

Software Design Specification for Digital Door Lock System

Project Team

Team1

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1 Introduction

1.1 Purpose

1.2 Scope

1.2.1 개발팀

Team 1

1.2.2 제한사항

HW 와 연동까지 고려하지 않고, SW 로만 구동할 수 있도록 한다.

1.2.3 제품의 활용도

개발이 완료된 후 실제 Digital Door Lock 의 Software 를 개발하기 위한 Prototype 으로 삼을 수 있다

1.2.4 개발환경

IDE: Eclipse CDT

Compiler: MinGW

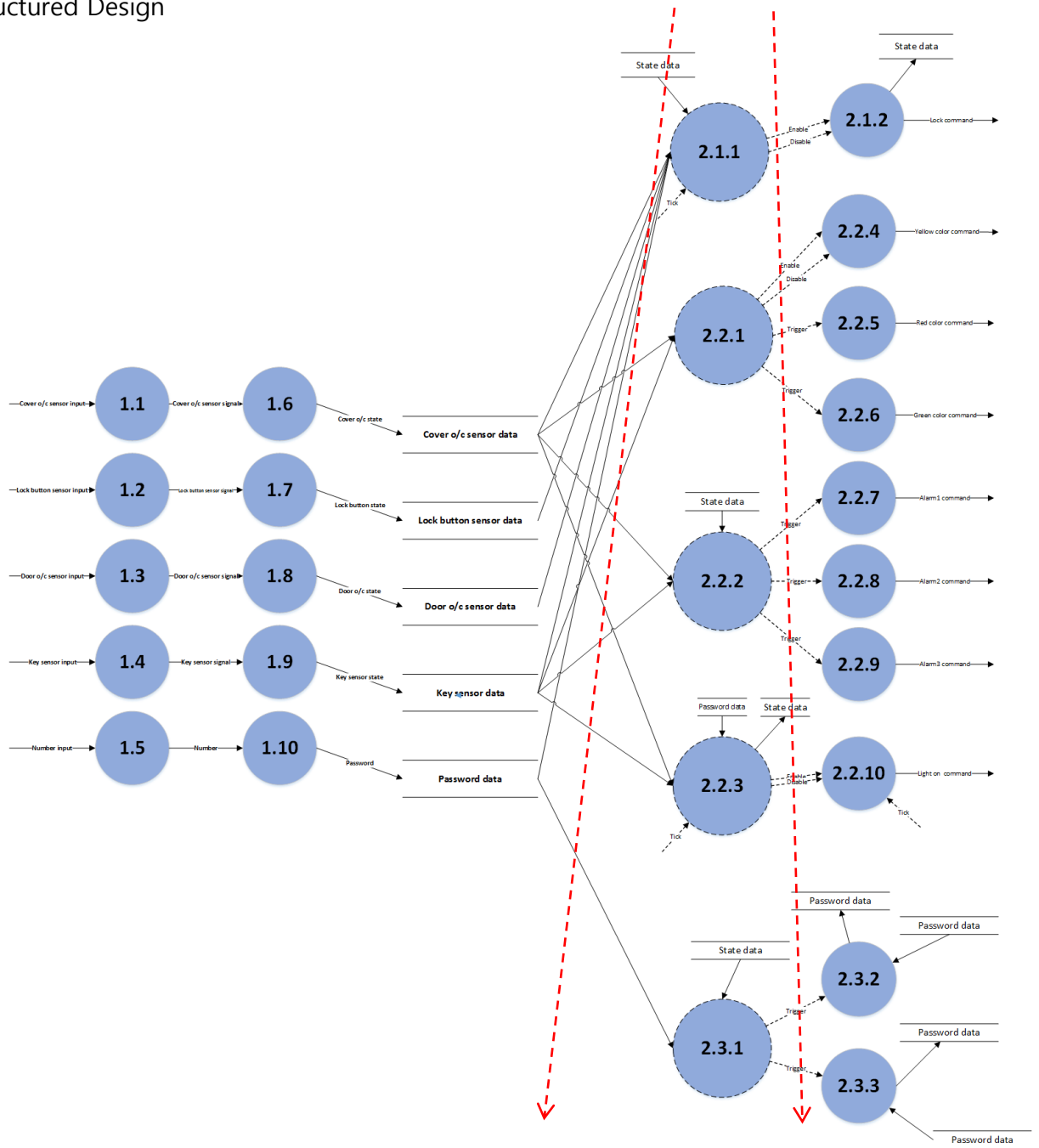
1.3 Definitions and acronyms

2 References

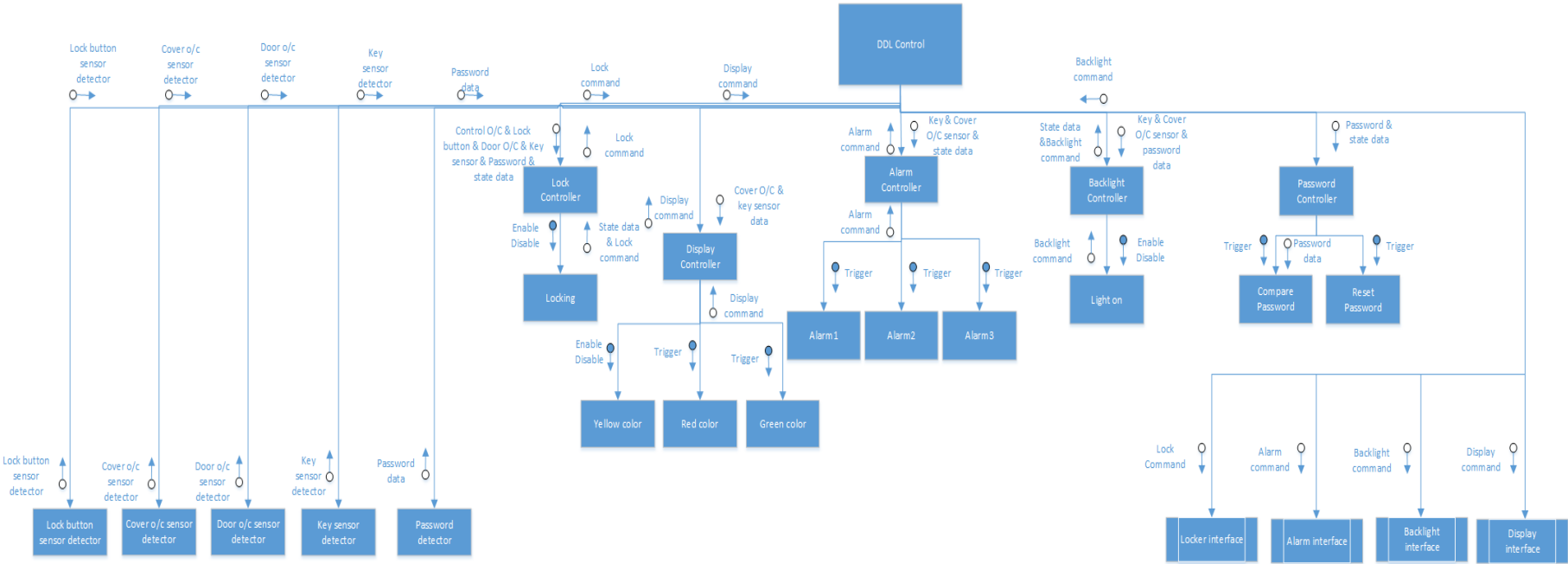
IEEE Std. 830-1998

Modern Structured Analysis by Edward Yourdon

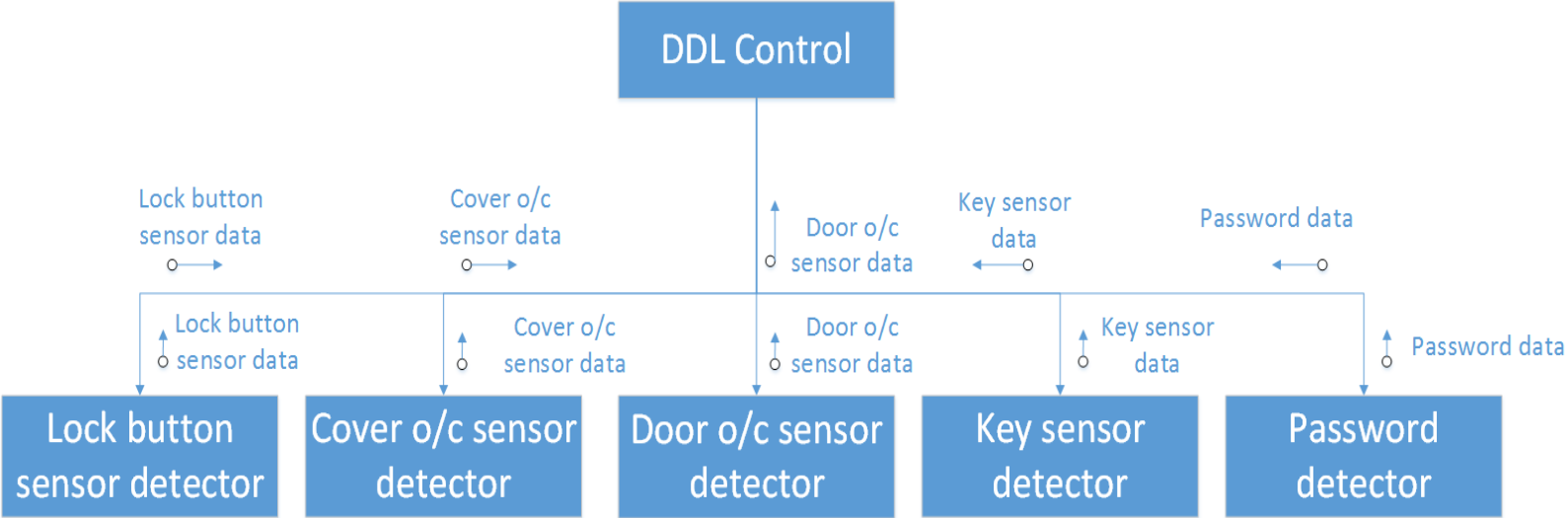
3 Structured Design



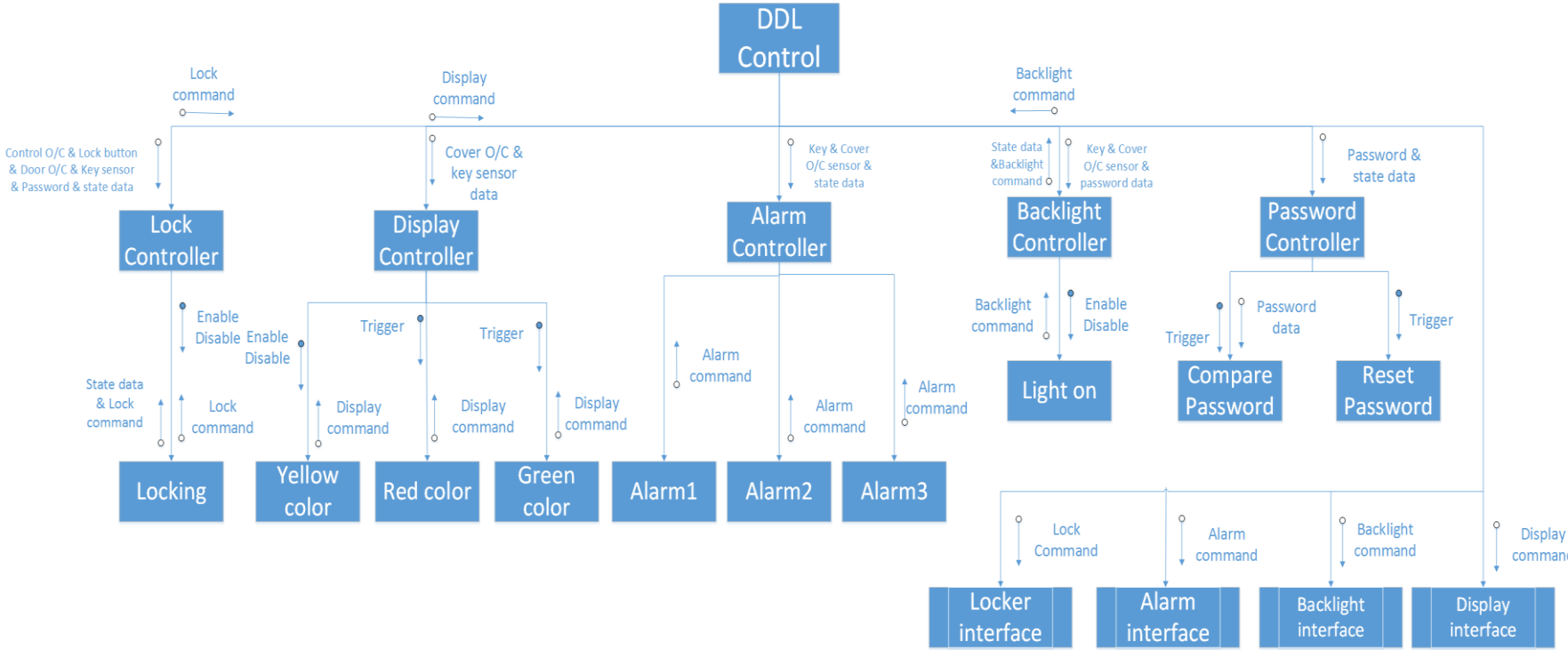
3.1 Structured Charts (Basic)



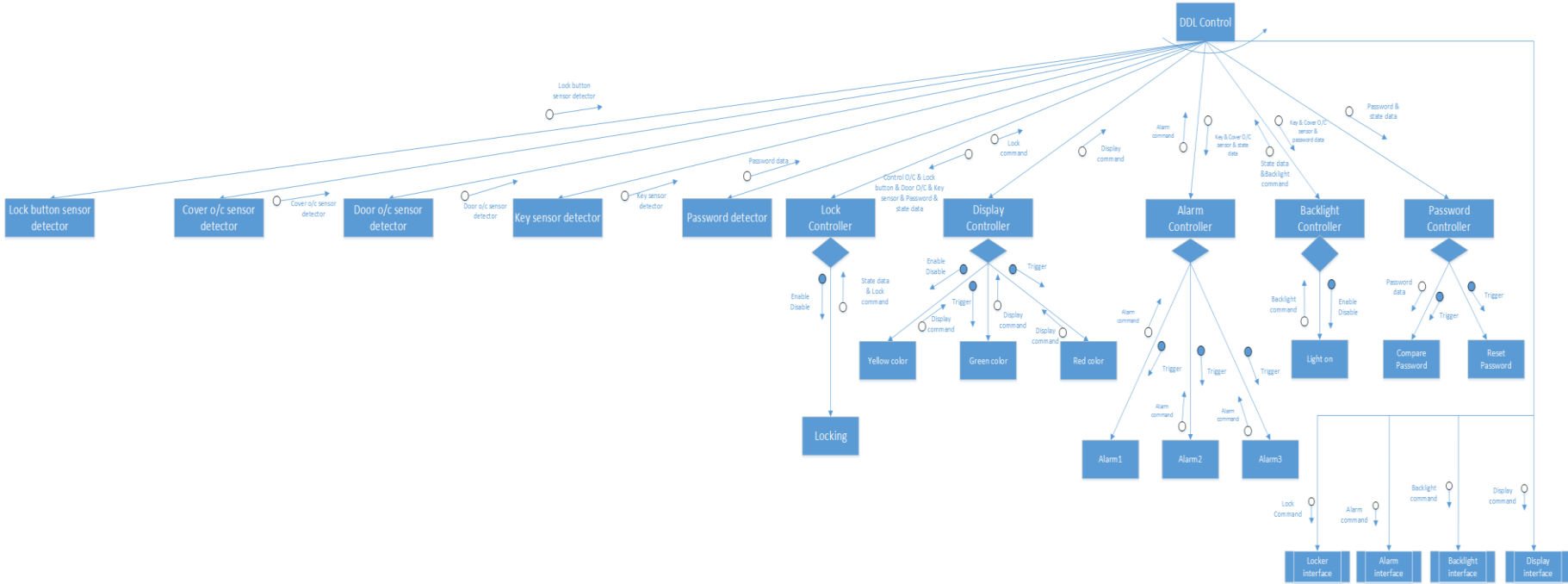
3.1.1 Basic(Input)



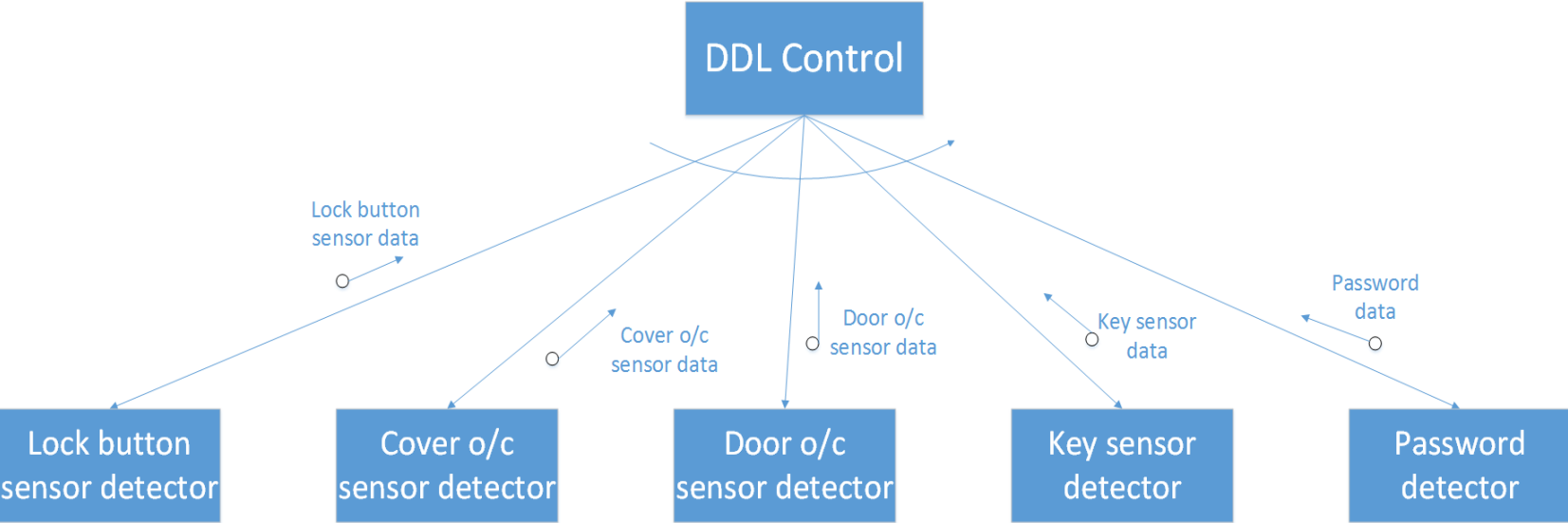
3.1.2 Basic(Output & Control)



3.2 Structured Charts (Advanced)



3.2.1 Advanced(Input)



3.2.2 Advanced(Output & Control)

